

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED  
JUL -3 2002  
TECHNOLOGY CENTER 2800

In re Application of

Kira OHTA, et al.

Serial No.: 09/717,215

Group Art Unit: 2817

Filed: November 22, 2000

Examiner: Joseph Chang

For: HIGH EFFICIENCY AMPLIFIER WITH AMPLIFIER ELEMENT, RADIO TRANSMISSION DEVICE  
THEREWITH AND MEASURING DEVICE THEREFORTHE COMMISSIONER FOR PATENTS AND TRADEMARKS  
Washington, DC 20231

Dear Sir:

Transmitted herewith is an Amendment in the above identified application.

☒  
☐  
☐

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

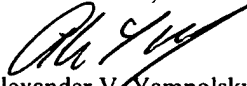
The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	67	67	0	\$18.00 =	\$0.00
Independent Claims	17	17	0	\$84.00 =	\$0.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL &amp; EMERY

  
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 Date: July 1, 2002

Docket No.: 49657-844

JUL 01 2002

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Akira OHTA, et al.

Serial No.: 09/717,215

Group Art Unit: 2817

Filed: November 22, 2000

Examiner: Joseph Chang

For: HIGH EFFICIENCY AMPLIFIER WITH AMPLIFIER ELEMENT, RADIO  
TRANSMISSION DEVICE THEREWITH AND MEASURING DEVICE  
THEREFOR

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AMENDMENT

Commissioner for Patents  
Washington, DC 20231

Sir:

In response to the Office Action dated April 3, 2002, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Second full paragraph on page 19 now reads as follows:

a1

Fig. 3 shows an example of a structure of a low impedance isolator 3A;

Fourth full paragraph on page 19 now reads as follows:

a2

Fig. 5 is a diagram referenced for describing impedance between high efficiency amplifier 1A and an isolator 3A;

Sixth full paragraph on page 19 now reads as follows:

a3

Fig. 7 is a diagram referenced for describing impedance between high efficiency amplifier 1A and an isolator 3A;